

12. (Amended) The adapter of claim 1, further comprising a data buffer connected to the processor configured to store information received from the electronic transaction device.
13. (Amended) The adapter of claim 12, wherein the data buffer is configured to purge at least a portion of the information received from the electronic transaction device after a predetermined period of time.
14. (Amended) The adapter of claim 12, wherein the data buffer is configured to purge at least a portion of the information received from the electronic transaction device after a predetermined number of data transfer operations.
15. (Amended) The adapter of claim 1, wherein the processor further comprises a data buffer adapted to store information received from the electronic transaction device.
16. (Amended) The adapter of claim 15, wherein the data buffer is configured to purge at least a portion of the information received from the electronic transaction device after a predetermined period of time.
17. (Amended) The adapter of claim 15, wherein the data buffer is configured to purge at least a portion of the information received from the electronic transaction device after a predetermined number of data transfer operations.
18. (Amended) An adapter for use with point of sale card readers, the adapter comprising:
- a) a housing, capable of being inserted in the card reader;
 - b) a transceiver in the housing the transceiver adapted to receive information from an electronic transaction device;
 - c) a processor in the housing connected to the transceiver;

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- d) an electromagnet configured to emulate a magnetic stripe in accordance with information received from an electronic transaction device connected to the processor.
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23. (Amended) The adapter of claim 18, further comprising a data buffer connected to the processor, the data buffer configured to purge at least a portion of the information received from the electronic transaction device after a predetermined period of time.

24. (Amended) The adapter of claim 18, further comprising a data buffer connected to the processor, the data buffer configured to purge at least a portion of the information received from the electronic transaction device after a predetermined number of data transfer operations.

Please cancel claims 40-43.